

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2830	(design same (feature\$2 or pattern)) same (offset)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/08 11:22
L2	19	((electronic adj circuit) same layout) and (design same (feature\$2 or pattern)) same (offset)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/08 11:28
L3	0	((electronic adj circuit) same layout) and (design same (feature\$2 or pattern) same template) same (offset)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/08 11:28
L4	2	(electronic adj circuit) and (design same (feature\$2 or pattern) same template\$2) same (offset)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/08 11:28
S1	1752	716/1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 14:22
S2	722	716/7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 14:08
S3	1161	716/8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 14:08
S4	3	(716/1).cccls. and (design adj feature\$2) and (template\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 14:12
S5	1	(716/7).cccls. and (design adj feature\$2) and (template\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 14:14
S6	2	(716/8).cccls. and (design adj feature\$2) and (template\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 14:22

S7	23	("716"/\$.ccls. and (design adj feature\$2) and (template\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 14:16
S8	667	(design adj feature\$2) and (template\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 14:17
S9	56	(design adj feature\$2) same (template\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 14:17
S10	2	(electronic adj circuit) and ((design adj feature\$2) same (template\$2))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 14:23
S11	976	716/12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 14:22
S12	1	(716/12).ccls. and (design adj feature\$2) and (template\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 14:22
S13	16	(electronic adj circuit) and ((design adj feature\$2) and (template\$2))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 14:49
S14	2	(electronic adj circuit) and ((design adj feature\$2) and (template\$2)) and (control adj point)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 14:53
S15	86	((design adj feature\$2) and (template\$2)) and (grid)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 14:53
S16	2	((design adj feature\$2) same (template\$2)) same (grid)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 14:53

S17	2	(feature\$2) and ((design adj instruction) adj file)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 15:00
S18	124	(feature\$2) and (design adj instruction)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 15:00
S19	8	(feature\$2) same (design adj instruction)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 15:01
S20	8719	(feature\$2) and (offset) and ((control or reference) adj point)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 15:03
S21	1631	(feature\$2) and (offset) and (control adj point)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 15:03
S22	47	(design adj feature\$2) and (offset) and (control adj point)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 15:04
S23	2	(design adj feature\$2) and ((offset) same (control adj point))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 15:04
S24	3	((recall\$4 or retrie\$) adj template) and (feature) and (design adj instruction\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 15:08
S25	36	(data adj structure) and (template\$2) and (offset same ((reference or control) adj point))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 17:46
S26	2	(electronic adj circuit) and (data adj structure) and (template\$2) and (offset same ((reference or control) adj point))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 17:54

S27	1	((electronic adj circuit) and (data adj structure) and (template\$2) and (offset same ((reference or control) adj point))).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 17:49
S28	1	((electronic adj circuit) and (design adj instruction) and (template\$2) and (offset same ((reference or control) adj point)) and grid).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 17:51
S29	3	(electronic adj circuit) and (template\$2) and (offset same ((reference or control) adj point))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 17:55
S30	1	(electronic adj circuit) and (template\$2) and (offset same ((reference or control) adj point)). CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 18:14
S31	1	((electronic adj circuit) and (template\$2) and (feature\$2 same offset)).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 18:21
S32	814	((electronic adj circuit) and (template\$2) and (feature\$2 or pattern\$2))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 18:22
S33	293	(electronic adj circuit) and (template\$2 same(feature\$2 or pattern\$2))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 18:22
S34	293	(electronic adj circuit) and (template\$2 same (feature\$2 or pattern\$2))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 18:25
S35	8	(electronic adj circuit) and (template\$2 same (feature\$2 or pattern\$2) same offset)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/07 18:26

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IEEE CNF IEEE Conference Proceeding

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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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 McIlrath, L.G.;  
 Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transacti  
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 Volume 47, Issue 12, Dec. 2000 Page(s):1353 - 1364  
 Digital Object Identifier 10.1109/82.899628

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- ☐ 2. **Symbolic match hypothesis evaluation for UHRR radar targets**  
 Fister, T.;  
 Aerospace and Electronics Conference, 1994. NAECON 1994., Proceedings o  
 National  
 23-27 May 1994 Page(s):696 - 700 vol.2  
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